



CEESIM-VPX

New RF subsystem design/package, CEESIM-VPX builds on existing CEESIM capabilities.

Features

- VPX Architecture
- Industry Leading RF Performance
- Plug N Play Auto Configuration
- Scalable Solution
- Simple Expansion in the Field
- User Reconfigurable





Parameter	CEESIM-VPX
Frequency Control	Direct Digital Synthesis
Tuning Time	0.5 usec
Frequency Resolution 20 MHz to 40 GHz	1 Hz
Frequency Accuracy	±2 Hz
Phase Noise @ SUT ports for 20 MHz-18 GHz	
1 kHz offset	-95 dBc/Hz
10 kHz offset	-110 dBc/Hz
100 kHz offset	-120 dBc/Hz
1 MHz offset	-130 dBc/Hz
10 MHz Offset	-137 dBc/Hz
Phase Noise @ SUT ports for 18-40 GHz	
1 kHz offset	≤-92 dBc/Hz
10 kHz offset	≤-107 dBc/Hz
100 kHz offset	≤-117 dBc/Hz
1 MHz offset	≤-125 dBc/Hz
10 MHz offset	≤-135 dBc/Hz
Broadband Noise @ CEESIM Output ports 20 MHz-40GHz	-85 dBc/MHz (typ)

Parameter	CEESIM-VPX
Spurious @ CEESIM Output ports 20MHz-40 GHz	-70 dBc (typ)
FMOP Deviation	±500 MHz
FMOP Accuracy	±1%
FMOP Unlock Offset	0 Hz
PMOP Resolution	1 degree
PMOP Accuracy	±2 degrees
Maximum MOP Sample Rate	1280 MSPS
MOP Pattern Playback Capacity Memory Stream I/Q from External Data Source	2 GB Yes
Preserve MOP pattern with TDOA	Yes
Phase Coherency	All emitters
Required Emitter Calibrations	None

For more information, please contact:

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